



JPW

PATENT  
Customer Number 22,852  
Attorney Docket No. 04329.2858-01000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)	
	)	
Kei WATANABE et al.	)	Allowed: March 15, 2005
	)	
Serial No.: 10/668,277	)	Group Art Unit: 2822
	)	
Filed: September 24, 2003	)	Examiner: M. Trinh
	)	
For: SEMICONDUCTOR DEVICE AND	)	Confirmation No.: 3508
MANUFACTURING METHOD	)	
THEREOF	)	

**Mail Stop ISSUE FEE**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**LETTER REQUESTING ACKNOWLEDGMENT  
OF SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT  
FILED UNDER 37 C.F.R. § 1.97(c)**

An Information Disclosure Statement was filed in this application on October 7, 2004. In reviewing our files, we find that we have not received from your Office a copy of the Form PTO-1449 which would serve as acknowledgment of receipt of the Information Disclosure Statement and as an indication that the document was considered by the Examiner.

A copy of the Information Disclosure Statement, the Information Disclosure Citation on Form PTO-1449, and of the postcard that was filed with the Information Disclosure Statement and was stamped by the U.S. Patent and Trademark Office to indicate receipt of the papers, is attached.

The Examiner is respectfully requested either to send undersigned a duplicate of the copy of the Form PTO-1449 or issue a Supplemental Notice of Allowability (PTOL-37) which both acknowledges receipt of the Information Disclosure Statement and the Information Disclosure Citation, and indicates that the document was considered by the Examiner.

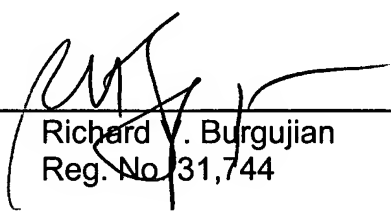
Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated:

4/6/05

By:

  
Richard V. Burgujian  
Reg. No. 31,744

RVB/FPD/acd



PATENT  
Customer No. 22,852  
Attorney Docket No. 04329.2858-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
Kei WATANABE, et al. ) Group Art Unit: 2822  
Application No.: 10/668,277 ) Examiner: Trinh, Michael Manh  
Filed: September 24, 2003 )  
For: SEMICONDUCTOR DEVICE AND )  
MANUFACTURING METHOD THEREOF )

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 C.F.R. § 1.97(c)**

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(c), Applicants bring to the attention of the Examiner the document listed on the attached PTO-1449 form. Applicants file this Information Disclosure Statement (IDS) after the period set forth in 37 C.F.R. § 1.97(b), but before the mailing date of either a Final action, Quayle action, or a Notice of Allowance, to the undersigned representative's knowledge at the time of filing this IDS. This IDS supplements the IDS filed on September 24, 2003.

Under the provisions of 37 C.F.R. § 1.97(c), this Supplemental Information Disclosure Statement includes the following certification as specified by Section 1.97(e): The document listed in this Information Disclosure Statement was first cited in a Korean Office Action mailed by the Korean Patent Office on August 5, 2004 in counterpart foreign Application No. 10-2002-0044737, and this Information Disclosure Statement is being filed within three months of the

mailing date of that communication. Applicants enclose a copy of the Korean Patent Office Action and its English translation.

Applicants respectfully request that the Examiner consider the listed document and indicate its consideration by making appropriate notation on the attached PTO-1449 form.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed document is material or constitutes "prior art." If the Examiner applies the document as prior art against any claims in the application and Applicants determine that the listed document does not constitute "prior art" under United States law, Applicants reserve the right to present to the Office the relevant facts and law regarding the appropriate status of the document.

Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed document, should the document be applied against the claims of the present application.

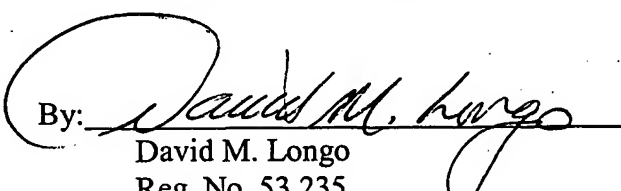
If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: October 7, 2004

By:

  
David M. Longo  
Reg. No. 53,235

## INFORMATION DISCLOSURE CITATION

Atty. Docket No.	04329.2858-01	Appln. No.	10/668,277
Applicant	Kei WATANABE, et al.		
Filing Date	September 24, 2003	Group:	2822

## U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
	6,159,661	12/2000	Huang et al.			

## FOREIGN PATENT DOCUMENTS

Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)


Examiner	Date Considered
<p>*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>	
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce



RUB/DMC

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**PLEASE STAMP TO ACKNOWLEDGE RECEIPT OF THE FOLLOWING:**

In Re Application of: Kei WATANABE, et al.

Application No.: 10/668,277

Group Art Unit: 2822

Filed: September 24, 2003

Examiner: Trinh, Michael Manh

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

1. Supplemental Information Disclosure Statement, including Form PTO-1449, a copy of Korean Patent Office Action mailed 8/5/04 and its English translation

Dated: October 7, 2004

Docket No.: 04329.2858-01

(Due Date: N/A)

DML/M. Murray - Mail Drop 749



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